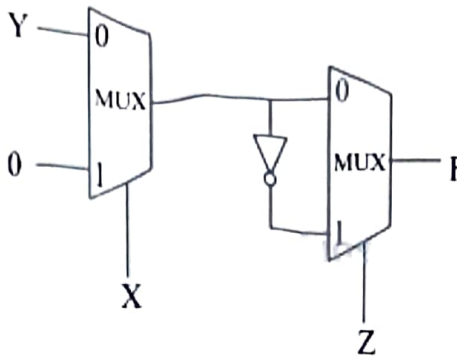




4	<p>Identify the given Boolean function (F) represented in terms of multiplexer and implement the function (F) using 2 to 4 decoder logic.</p> 	10
5	<p>Design a combinational circuit with least number of logic gates which takes input from 0 to 9 and provides output = input + 4. Write the Verilog code for designed combinational circuit in data flow modelling.</p>	10