NAND & NOR GATE IMPLEMENTATION

STEP TO GET NAND- NAND LOGIC

- **STEP 1:** Draw the **logic circuit using AND, OR and NOT GATES** for the given/ Obtained B.E.
- **STEP 2:** Put Bubbles at **input of OR** and at the **output of AND**.
- **Step 3:** Insert **Inverter (NOT GATE) for each bubbles**. (i.e. before NOR and After NAND)
- **Step 4:** Remove **double NOT gates / two NOT gates** connected in series (adjacent), if any
- **Step 5:** Replace **bubbled OR, Inverter** by NAND.

STEP TO GET NOR- NOR LOGIC

- **STEP 1**: Draw the **logic circuit using AND, OR and NOT GATES** for the given/ Obtained B.E
- **STEP 2**: Put Bubbles at **input of AND** and at the **output of OR**.
- **Step 3**: Insert **Inverter (NOT GATE) for each bubbles**. (i.e. before NOR and After NAND)
- **Step 4**: Remove **double NOT gates / two NOT gates** connected in series (adjacent), if any
- **Step 5**: Replace **bubbled AND, Inverter** by **NOR**.