



# VIT<sup>®</sup>

Vellore Institute of Technology  
(Deemed to be University under section 3 of UGC Act, 1956)

REG.NO.: 24BV00214

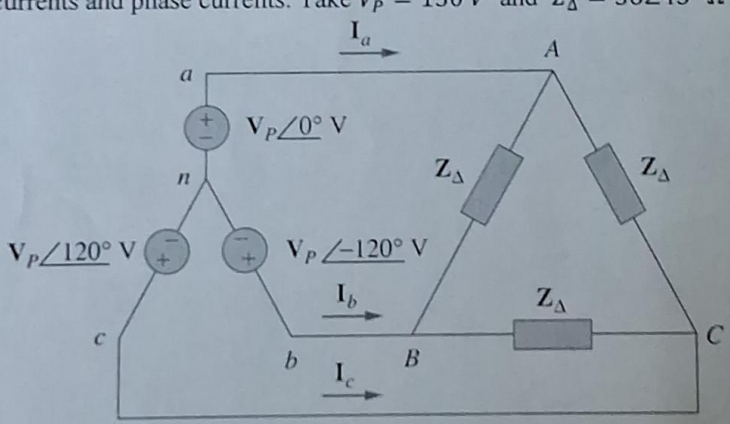
SCHOOL OF ELECTRONICS ENGINEERING  
CONTINUOUS ASSESSMENT TEST - II  
FALL SEMESTER 2024-2025

SLOT:A1+TA1

**Programme Name & Branch** : B Tech, (ECE, VLSI)  
**Course Code and Course Name** : BEEE102L, Basic Electrical and Electronics Engineering  
**Faculty Name(s)** : Dr. Kathirvelan J, Dr Somasundaram D, Dr Sagar, Dr Vaegae Naveen kumar, Dr V Velmurugan  
**Class Number(s)** : 8645, 6516, 6521, 6515, 6506  
**Date of Examination** : 13-10-2024  
**Exam Duration: 90 minutes** **Maximum Marks: 50**

**General instruction(s):**

- Answer All Questions
- M - Max mark; CO - Course Outcome; BL - Blooms Taxonomy Level (1 - Remember, 2 - Understand, 3 - Apply, 4 - Analyse, 5 - Evaluate, 6 - Create)
- Course Outcomes  
 CO 1. Evaluate DC and AC circuit parameters using various laws and theorems  
 CO 2. Comprehend the parameters of magnetic circuits  
 CO 4. Design basic combinational circuits in digital system

Q. No	Question	M	CO	BL																				
✓ 1.	In the balanced three-phase wye-delta circuit in figure 1, determine the line currents and phase currents. Take $V_p = 150\text{ V}$ and $Z_\Delta = 36\angle 45^\circ \Omega$ .  <p style="text-align: center;">Figure 1</p>	10	1	BL3																				
2.	✓ a). 2's complement representation of a 16-bit number (one sign bit and 15 magnitude bits) is FFF1. Its magnitude in decimal representation is _____ _____	2	4	BL3																				
	✓ b). Match the List I and List II <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th colspan="2">List I ( Binary)</th> <th colspan="2">List - II(Hexadecimal)</th> </tr> </thead> <tbody> <tr> <td>A</td> <td>10101010</td> <td>1.</td> <td>FF</td> </tr> <tr> <td>B</td> <td>11111111</td> <td>2.</td> <td>88</td> </tr> <tr> <td>C</td> <td>10001000</td> <td>3.</td> <td>92</td> </tr> <tr> <td>D</td> <td>10010010</td> <td>4.</td> <td>AA</td> </tr> </tbody> </table>	List I ( Binary)		List - II(Hexadecimal)		A	10101010	1.	FF	B	11111111	2.	88	C	10001000	3.	92	D	10010010	4.	AA	4		
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	<input checked="" type="checkbox"/> Carryout the following conversion, (i) Add $(-6)_{10}$ to $(+15)_{10}$ . Use eight bits (byte) to represent the signed numbers. (ii) Add $(-15)_{10}$ to $(+6)_{10}$ . Use eight bits (byte) to represent the signed numbers.	4		
3.	<input checked="" type="checkbox"/> Express the given Boolean expression $Y = \bar{A} + B\bar{C}$ in standard canonical SOP form.	4	4	BL3
	<input checked="" type="checkbox"/> Simplify the given functions using Boolean algebra and implement the same with the basic logic gates. $\bar{A}BC + A\bar{B}C + ABC + ABC$	6		
<input checked="" type="checkbox"/> 4.	Simplify the following Boolean expression using the 4-variable K-map. $F(A,B,C,D) = \sum m(2,3,6,7,8,10,13,15)$	10	4	BL3
5.	An iron ring with a cross-sectional area of $3 \text{ cm}^2$ and a mean circumference of 15 cm is wound with 250 turns of wire carrying a current of 0.3 A. The relative permeability of the ring is 1500. <input checked="" type="checkbox"/> Calculate the flux established in the ring. <input checked="" type="checkbox"/> If a saw cut of width 2 mm is made in the above ring, find the new value of the flux in the circuit.	10	2	BL3