VIT
Vellore Institute of Technology
(Deemed to be University under section 3 of UGC Act, 1956)

Vellore – 632014, Tamil Nadu, India

SCHOOL OF ELECTRICAL ENGINEERING

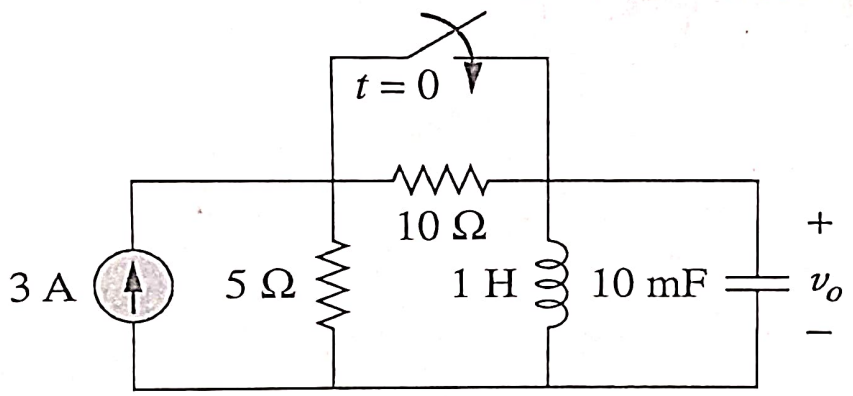
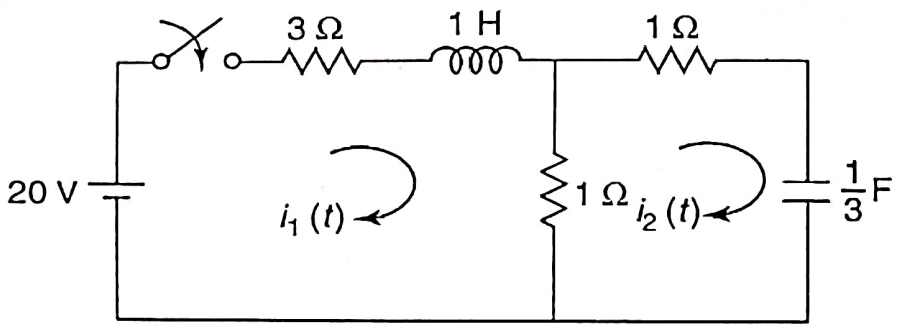
FALL SEMESTER 2025-2026

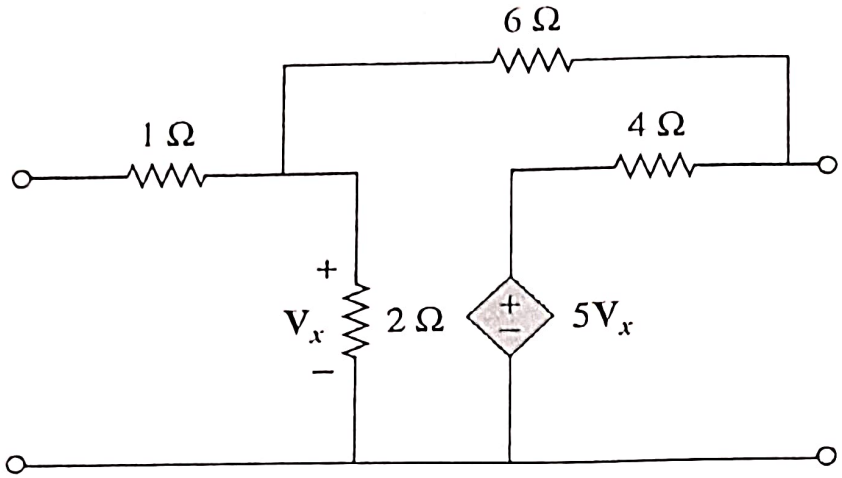
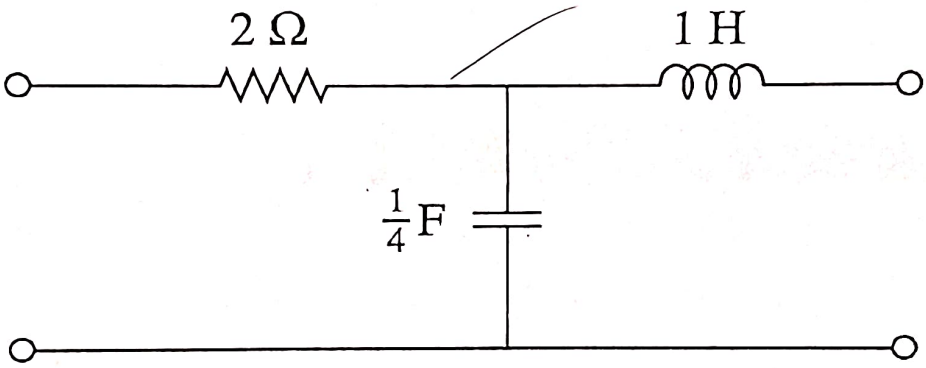
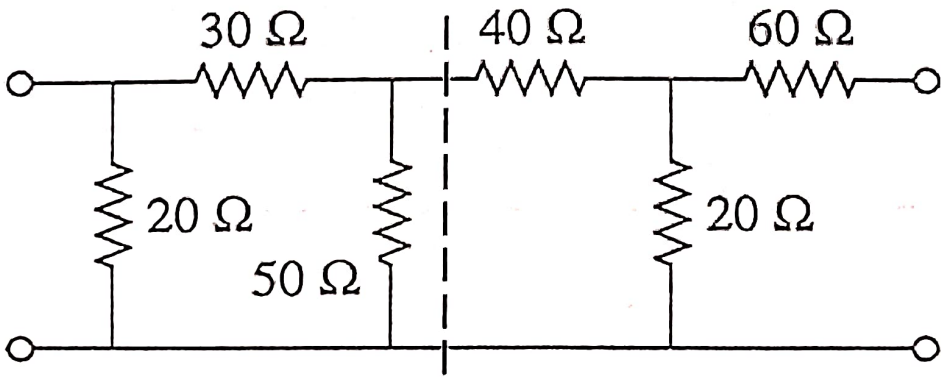
CAT-II

SLOT: A2+TA2+TAA2

Programme Name & Branch:	**B.Tech (EEE,BECS & EIE)**	Course Code:	**BEEE203L**
Course Name:	**Circuit Theory**	Class Number(s):	**VL2025260101020, 1021, 1019**
Faculty Members:	**Dr. Vijayakumar D, Dr. Janaki M, Dr. Belwin Edward**	Duration:	**90 Mins**
Date of the Examination:	**5th October 2025**	Max. Marks:	**50**

General Instructions: Answer all the questions

Q. No	Questions	Marks	CO	BL
1.	Find the output voltage $V_o(t)$ for t>0 in the circuit shown below. 	10	3	3
2.	In the network shown in below, the switch is closed at t = 0. Find current i_1 (t) for t > 0. What is its value in steady state for t>0? 	10	3	3

3.	Find the parameter equations that relate input voltage and output current to input current and output voltage for the given two port network. 	10	5	3
4.	For the network shown below, Calculate the parameters relate the currents at the ports of a two-port network to the voltages at the ports. 	10	5	3
5.	Obtain the impedance parameters representation of the two port network with sub-networks and also considering the interconnections network as shown dotted lines below. 	10	5	3