

**VIT**

Vellore Institute of Technology

Established on 15th January 1984 under section 3 of UGC Act, 1956

REG.NO.:

SCHOOL OF ELECTRICAL ENGINEERING
CONTINUOUS ASSESSMENT TEST - II
WINTER SEMESTER 2024-2025

SLOT: C2

Programme Name & Branch : B.Tech. EEE & EIE
 Course Code and Course Name : BEEE205L - Electronic Devices and Circuits
 Faculty Name(s) : Dr. N. Sudhakar & Dr. G. Arunkumar
 Class Number(s) : VL2025260101212 & VL2025260101213
 Date of Examination : 07-10-2025, Tuesday, 09.30 AM to 11.00 AM
 Exam Duration : 90 minutes
 Maximum Marks: 50

General instruction(s):

- Answer All Questions
- M - Max mark; CO - Course Outcome; BL - Blooms Taxonomy Level (1 - Remember, 2 - Understand, 3 - Apply, 4 - Analyse, 5 - Evaluate, 6 - Create)
- Course Outcomes (**Type the CO statements covered in this question paper. Use the CO number as per the syllabus copy**)
 CO2 - Analyze and design BJT and MOSFET DC circuits and their amplifier configurations

Q. No	Question	M	CO	BL
1.	Design and analyze a BJT Common-Emitter Voltage Divider Biased (CE-VDB- with full bypass capacitor) amplifier using the following parameters: $V_{CC} = 20\text{ V}$, $\beta = 200$, $R_1 = 180\text{ k}\Omega$, $R_2 = 27\text{ k}\Omega$, $R_E = 1.0\text{ k}\Omega$ with full bypass capacitor $C_E = 470\text{ }\mu\text{F}$, $R_C = 3.9\text{ k}\Omega$, Source resistance $R_S = 600\text{ }\Omega$ (in series with AC input), Load resistance $R_L = 10\text{ k}\Omega$, Coupling capacitors: $C_{in} = 4.7\text{ }\mu\text{F}$, $C_{out} = 10\text{ }\mu\text{F}$. Task: Determine the AC output voltage v_{out} for a sinusoidal input $v_{in} = 10\text{ mV}$. Clearly show: 1. DC bias point calculations 2. AC small-signal model parameters 3. Final voltage gain and output voltage.	10	2	3
2.	Design and analyze a BJT Common-Emitter Voltage Divider Biased (CE-VDB- no bypass capacitor) amplifier using the following parameters: $V_{CC} = 18\text{ V}$, $\beta = 180$, $R_1 = 150\text{ k}\Omega$, $R_2 = 33\text{ k}\Omega$, $R_E = 1.2\text{ k}\Omega$ (no bypass capacitor), $R_C = 4.7\text{ k}\Omega$, Load resistance $R_L = 6.8\text{ k}\Omega$, Coupling capacitors: $C_{in} = 3.3\text{ }\mu\text{F}$, $C_{out} = 10\text{ }\mu\text{F}$. Assume $V_{BE} = 0.7\text{ V}$, $V_T = 25\text{ mV}$. Task: For a sinusoidal input, $v_{in} = 15\text{ mV}$, determine the AC output voltage v_{out} . Clearly show: 1. DC bias point calculations 2. AC small-signal model parameters 3. Final voltage gain and output voltage.	10	2	3
3.	Design and analyze a BJT Common-Emitter Voltage Divider Biased (CE-VDB- swamped) amplifier using the following parameters: $V_{CC} = 18\text{ V}$, $\beta = 180$, $R_1 = 150\text{ k}\Omega$, $R_2 = 27\text{ k}\Omega$, $R_{E1} = 47\text{ }\Omega$ (unbypassed), $R_{E2} = 1\text{ k}\Omega$ parallel with $C_E = 220\text{ }\mu\text{F}$ (bypassed for AC), $R_C = 4.3\text{ k}\Omega$, Source resistance	10	2	3



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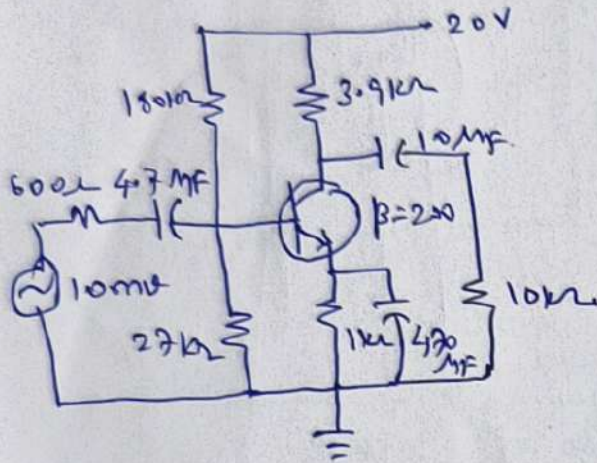
SCHOOL OF ELECTRICAL ENGINEERING CONTINUOUS ASSESSMENT TEST - II WINTER SEMESTER 2024-2025

SLOT: C2

	$R_S = 600 \Omega$ (in series with AC input), Load resistance $R_L = 8.2 \text{ k}\Omega$, Coupling capacitors: $C_{in} = 3.3 \mu\text{F}$, $C_{out} = 6.8 \mu\text{F}$. Assume $V_{BE} = 0.7 \text{ V}$, $V_T = 25 \text{ mV}$. Task: For a sinusoidal input, $v_{in} = 12 \text{ mV}$, determine the AC output voltage v_{out}. Clearly show: - DC bias point calculations - AC small-signal model parameters - Final voltage gain and output voltage.			
4.	Perform a DC operating-point (Q-point) analysis of the N-channel Enhancement MOSFET (E-MOSFET) circuit. The MOSFET source is grounded (no source resistance). A voltage divider biases the gate. $V_{DD} = 12 \text{ V}$, $R_1 = 1 \text{ M}\Omega$ (to $+V_{DD}$), $R_2 = 270 \text{ k}\Omega$ (to ground), $R_D = 2.2 \text{ k}\Omega$, $V_{Th} = 1.6 \text{ V}$, Transconductance parameter: $k = 2.0 \text{ mA/V}^2$. Assume ideal gate current $I_G = 0 \text{ A}$. Task (show all steps & equations): - Compute the gate voltage V_G and V_{GS} (note: $V_S = 0$) - Compute the drain current I_D and V_{DS} - Device power $P_D = I_D \cdot V_{DS}$.	10	2	3
5.	Perform a DC operating-point (Q-point) analysis of the N-channel Enhancement MOSFET (E-MOSFET) circuit. The MOSFET source is connected to the ground through R_S. A voltage divider biases the gate. $V_{DD} = 15 \text{ V}$, $R_1 = 820 \text{ k}\Omega$ (to $+V_{DD}$), $R_2 = 220 \text{ k}\Omega$ (to ground), $R_D = 3.9 \text{ k}\Omega$, $R_S = 470 \Omega$, $V_{Th} = 2 \text{ V}$, Transconductance parameter: $k = 1.5 \text{ mA/V}^2$. Assume ideal gate current $I_G = 0 \text{ A}$. Task (show all steps & equations): - Compute the gate voltage V_G from the divider, and hence V_{GS} - Compute the drain current I_{DQ} and V_{GSQ} - Find V_{DS}.	10	2	3

BEEE 205L - EDC Key:

- ① $V_{CC} = 20V$, $\beta = 200$, $R_1 = 180K\Omega$, $R_2 = 27K\Omega$, $R_E = 1K\Omega$, $C_E = 470\mu F$
 $R_C = 3.9K\Omega$, $R_S = 600\Omega$, $R_L = 10K\Omega$, $C_{in} = 4.7\mu F$, $C_o = 10\mu F$, $V_{in} = 10mV$



$$V_{Th} = \frac{V_{CC} \cdot R_2}{R_1 + R_2} = \frac{(20)(27K)}{(180K + 27K)} = 2.608V$$

$$R_{Th} = R_1 \parallel R_2 = 180K \parallel 27K = 23.478K\Omega$$

$$\beta R_E = 10K\Omega$$

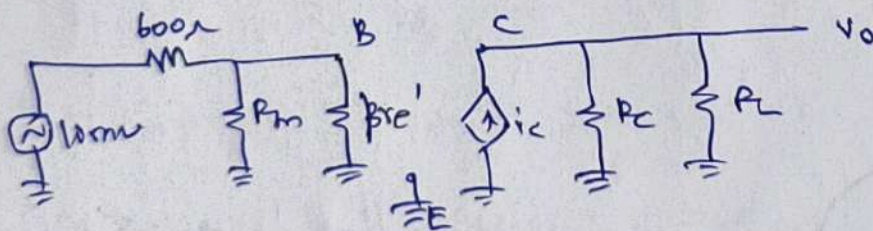
$$(200)(1K) < 10(27K); \beta \neq 1 + \beta, I_C \neq I_E$$

$$I_B = \frac{V_{Th} - V_{BE}}{R_{Th} + (1 + \beta)R_E} = \frac{2.608 - 0.7}{(23.478K) + (200 + 1)(1K)} = 8.499\mu A$$

$$I_C = \beta I_B = (200)(8.499\mu A) = 1.699mA$$

$$I_E = (1 + \beta) I_B = (201)(8.499\mu A) = 1.708mA$$

$$r_e' = \frac{26mV}{I_E} = \frac{26mV}{1.708mA} = 15.222\Omega, \quad \beta r_e' = (200)(15.222) = 3.044K\Omega$$



$$Z_i(\text{base}) = \beta r_e' = 3.044K\Omega$$

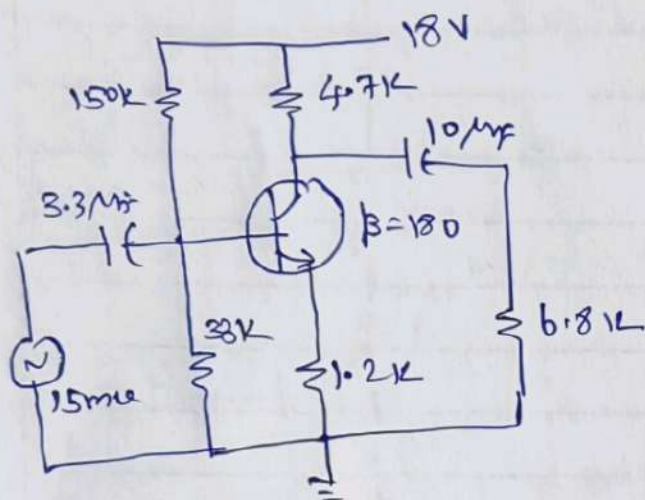
$$Z_i(\text{stage}) = R_{Th} \parallel \beta r_e' = 23.478K \parallel 3.044K = 2.694K\Omega$$

$$r_c = R_C \parallel R_L = 3.9K \parallel 10K = 2.805K\Omega$$

$$V_i = \frac{V_s \cdot Z_i(\text{stage})}{R_S + Z_i(\text{stage})} = \frac{(10m)(2.694K)}{600 + (2.694K)} = 8.153mV$$

$$A_v = \frac{r_c}{r_e'} = \frac{2.805K}{15.222} = 184.272, \quad V_o = A_v \cdot V_i = 1.502V$$

② $V_{CC} = 18V$, $\beta = 180$, $R_1 = 150k\Omega$, $R_2 = 33k\Omega$, $R_E = 1.2k\Omega$, $R_C = 4.7k\Omega$
 $R_L = 6.8k\Omega$, $C_{in} = 3.3\mu F$, $C_O = 10\mu F$, $V_{BE} = 0.7V$, $V_T = 25mV$, $V_{in} = 15mV$



$$V_{Th} = \frac{V_{CC} \cdot R_2}{R_1 + R_2} = \frac{(18)(33k)}{150k + 33k} = 3.245V$$

$$R_{Th} = R_1 \parallel R_2 = 150k \parallel 33k = 27.049k\Omega$$

$$\beta R_E > 10 R_{Th} \quad (180)(1.2k) > 10(27.049k); \quad \beta \neq 1 + \beta$$

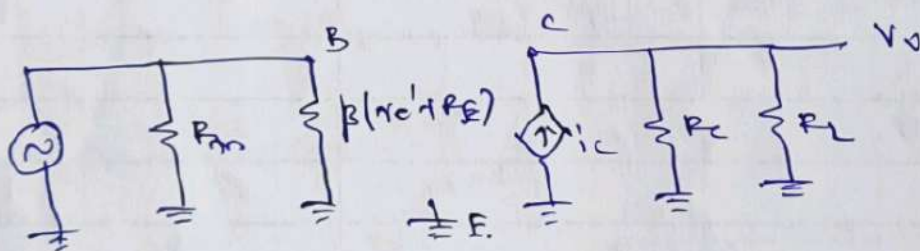
$$I_C \neq I_E$$

$$I_B = \frac{V_{Th} - V_{BE}}{R_{Th} + (1 + \beta)R_E} = \frac{3.245 - 0.7}{(27.049k) + (181)(1.2k)} = 10.419\mu A$$

$$I_C = \beta I_B = (180)(10.419\mu A) = 1.875mA$$

$$I_E = (1 + \beta) I_B = (181)(10.419\mu A) = 1.885mA$$

$$r_e' = \frac{V_T}{I_E} = \frac{25mV}{1.885mA} = 13.262\Omega, \quad \beta(r_e' + R_E) = 218.387k\Omega$$



$$Z_i(\text{base}) = \beta(r_e' + R_E) = 218.387k\Omega$$

$$Z_i(\text{stage}) = R_{Th} \parallel Z_i(\text{base}) = 27.049k \parallel 218.387k = 24.067k\Omega$$

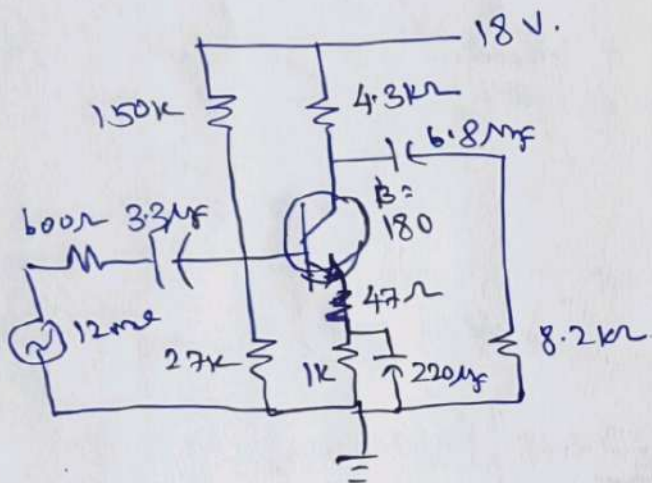
$$r_c = R_C \parallel R_L = 4.7k \parallel 6.8k = 2.779k\Omega$$

$$V_i = 15mV$$

$$A_v = \frac{r_c}{r_e' + R_E} = \frac{2.779k}{(13.262 + 1200)} = 2.29$$

$$\therefore V_o = A_v \cdot V_i = (15mV)(2.29) = 34.35mV$$

③ $V_{CC} = 18V$, $\beta = 180$, $R_1 = 150k\Omega$, $R_2 = 27k\Omega$, $R_{E1} = 47\Omega$, $R_{E2} = 1k\Omega$,
 $C_E = 220\mu F$, $R_C = 4.3k\Omega$, $R_S = 600\Omega$, $R_L = 8.2k\Omega$, $C_{in} = 3.3\mu F$,
 $C_O = 6.8\mu F$, $V_{BE} = 0.7V$, $V_T = 25mV$, $V_{in} = 12mV$



$$V_{th} = \frac{V_{CC} \cdot R_2}{R_1 + R_2} = \frac{(18)(27k)}{150k + 27k} = 2.745V$$

$$R_{th} = R_1 \parallel R_2 = 150k \parallel 27k = 22.881k\Omega$$

$$\beta R_E = 10k\Omega$$

$$(80)(1047) < (10)(27k)$$

$$\therefore \beta \neq 1 + \beta, I_C \neq I_E$$

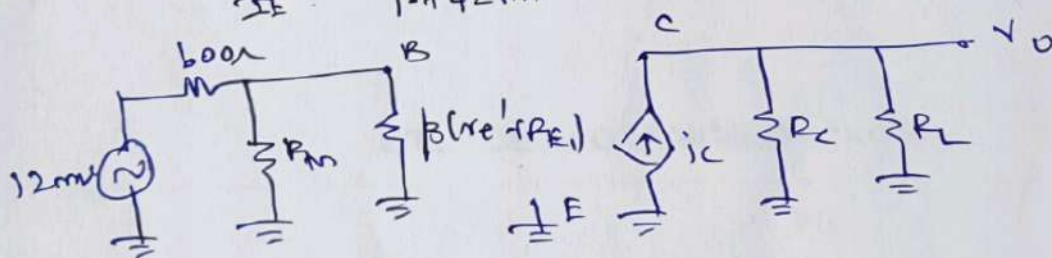
$$R_E = R_{E1} + R_{E2} = 1047\Omega$$

$$I_B = \frac{V_{th} - V_{BE}}{R_{th} + (1 + \beta) R_E} = \frac{2.745 - 0.7}{22.881k + (181)(1.047k)} = 9.628 \mu A$$

$$I_C = \beta I_B = (180)(9.628 \mu A) = 1.733 mA$$

$$I_E = (1 + \beta) I_B = (181)(9.628 \mu A) = 1.742 mA$$

$$r_e' = \frac{V_T}{I_E} = \frac{25mV}{1.742mA} = 14.351\Omega, \quad \beta(r_e' + R_{E1}) = (180)(61.351) = 11.043k\Omega$$



$$Z_i(\text{base}) = \beta(r_e' + R_{E1}) = 11.043k\Omega$$

$$Z_i(\text{stage}) = R_{th} \parallel Z_i(\text{base}) = 22.881k \parallel 11.043k = 7.448k\Omega$$

$$r_c = R_C \parallel R_L = 4.3k \parallel 8.2k = 2.82k\Omega$$

$$V_i = \frac{V_s \cdot Z_i(\text{stage})}{R_S + Z_i(\text{stage})} = \frac{(12m)(7.448k)}{600 + 7.448k} = 11.105mV$$

$$A_v = \frac{r_c}{r_e' + R_{E1}} = \frac{2.82k}{11.043k} = 0.255, \quad V_o = A_v \cdot V_i = (0.255)(11.105m) = 2.831mV$$

④ $V_{DD} = 12V$, $R_1 = 1M\Omega$, $R_2 = 270K\Omega$, $R_D = 2.2K\Omega$, $V_{th} = 1.6V$,
 $K = 2.0 \text{ mA/V}^2$, $I_G = 0A$.

DC. Analysis: $R_S = 0\Omega$. (X)

$$V_{G_S} = V_{G_D} = \frac{V_{DD} \cdot R_2}{R_1 + R_2} = \frac{(12)(270K)}{(1M) + (270K)} = 2.551V.$$

$$K = 2.0 \text{ mA/V}^2$$

$$I_{DQ} = K [V_{G_S} - V_{th}]^2$$

$$= (2.0 \text{ mA/V}^2) [2.551 - 1.6]^2$$

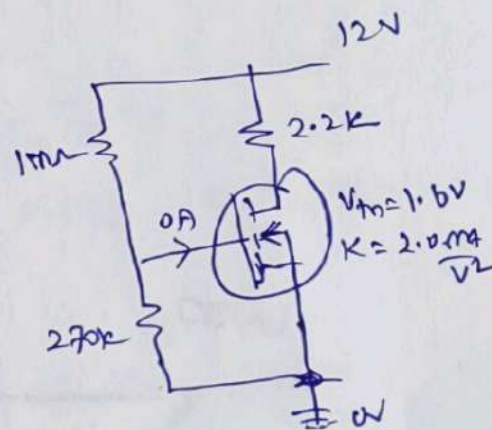
$$I_{DQ} = 1.808 \text{ mA.}$$

$$V_{D_S} = V_{DD} - I_{DQ} \cdot R_D$$

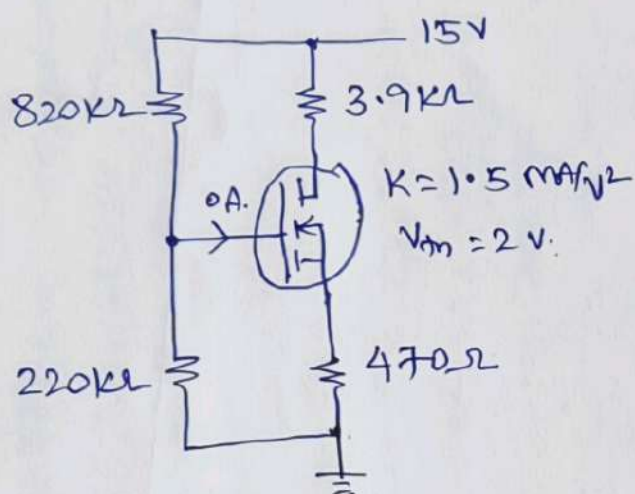
$$= 12 - (1.808 \text{ mA})(2.2K)$$

$$V_{D_S} = 8.022V.$$

$$P_D = I_D \cdot V_{D_S} = (1.808 \text{ mA})(8.022) = 14.503 \text{ mW.}$$



- ⑤ $V_{DD} = 15V$, $R_1 = 820K\Omega$, $R_2 = 220K\Omega$, $R_D = 3.9K\Omega$,
 $R_S = 470\Omega$, $V_{th} = 2V$, $K = 1.5mA/V^2$, $I_S = 0A$.



$$V_G = \frac{V_{DD} \cdot R_2}{R_1 + R_2} = \frac{(15)(220K)}{820K + 220K}$$

$$V_G = 3.173V$$

St. line:

$$V_{GS} = V_G - I_D R_S$$

$$V_{GS} = 3.173 - I_D (470)$$

When $V_{GS} = 0 \Rightarrow I_D = 6.751mA$.
 When $I_D = 0 \Rightarrow V_{GS} = 3.173V$ } St. line points.

Transfer Curve:

$$I_D = K [V_{GS} - V_{th}]^2$$

$$V_{GS} = 2V \Rightarrow I_D = K [2 - 2]^2 = 0mA$$

$$V_{GS} = 3V \Rightarrow I_D = (1.5 \times 10^{-3}) (3 - 2)^2 = 1.5mA$$

$$V_{GS} = 4V \Rightarrow I_D = (1.5 \times 10^{-3}) (4 - 2)^2 = 6mA$$

$$V_{GS} = 2.5V \Rightarrow I_D = (1.5 \times 10^{-3}) (2.5 - 2)^2 = 0.375mA$$

$$V_{GS} = 2.6V \Rightarrow I_D = (1.5 \times 10^{-3}) (2.6 - 2)^2 = 0.54mA$$

$$V_{GS} = 2.7V \Rightarrow I_D = (1.5 \times 10^{-3}) (2.7 - 2)^2 = 0.735mA$$

$$V_{GS} = 2.8V \Rightarrow I_D = (1.5 \times 10^{-3}) (2.8 - 2)^2 = 0.96mA$$

Load Graph: $I_{DQ} = 0.85mA$, $V_{GSQ} = 2.76V$.

$$\therefore V_{DS} = V_{DD} - I_{DQ} (R_D + R_S) = 15 - (0.85mA)(4.37K) = 3.714V$$

