

Course Code	Course Title	L	T	P	C
BITE406L	Parallel Computing	3	0	0	3
Pre-requisite	NIL	Syllabus version			
		1.0			
<b>Course Objectives:</b>					
<ol style="list-style-type: none"> <li>1. To understand the parallelization of basic mathematical and engineering algorithms.</li> <li>2. To learn the contemporary parallel architectures and their programming.</li> </ol>					
<b>Course Outcomes:</b>					
<ol style="list-style-type: none"> <li>1. Investigate the applicability of the basic parallel algorithms in solving complex problems</li> <li>2. Design efficient algorithms for a given parallel architecture and processor network</li> <li>3. Analyse the different algorithm designs for performing the key compute-intensive operations</li> <li>4. Use OpenMP, MPI libraries to implement the parallel algorithms</li> <li>5. Engage in individual study to write abstract of research paper related to parallel algorithms.</li> </ol>					
<b>Module:1</b>	<b>PRAM Algorithms</b>	<b>6 hours</b>			
Basics of Parallel Processing - Introduction to Flynn's Taxonomy - PRAM Model of Parallel Computation – EREW, CREW, CRCW - Mapping Theorem - Parallel Reduction - Prefix Sums - List Ranking - Preorder Tree Traversal - Merging Two Sorted Lists - Graph Coloring - Reducing Processors - Brent's Theorem.					
<b>Module:2</b>	<b>Processor Networks and Processor-Task Mapping</b>	<b>7 hours</b>			
Mesh Networks - Binary Tree - Hyper Tree – Pyramid – Butterfly – Hypercube - Cube Connected Cycles and Shuffle Exchange Networks - De Bruijn networks - Mapping Data to Processors: Embedding, Dilation, Ring to 2D mesh, 2D mesh to 2D mesh, Binary tree to 2D mesh, Binomial tree to 2D mesh - Embedding Graphs to Hypercubes: Binary Tree to Hypercubes, Binomial Tree to Hypercubes, Rings and Mesh to Hypercubes.					
<b>Module:3</b>	<b>Summation Algorithms</b>	<b>6 hours</b>			
Hypercube SIMD Model – Shuffle Exchange SIMD Summation Algorithm - 2D Mesh SIMD Summation Algorithm - UMA Summation Model – Broadcast - Binomial Tree Communication Pattern.					
<b>Module:4</b>	<b>Matrix Multiplication Algorithms</b>	<b>6 hours</b>			
Matrix Multiplication on 2D Mesh SIMD Model - Hypercube SIMD Model – Shuffle-Exchange SIMD Model - UMA Multiprocessor - Block Matrix Multiplication - Algorithms for Multicomputer - Row-column and Block-oriented Algorithms.					
<b>Module:5</b>	<b>Sorting</b>	<b>6 hours</b>			
Enumeration Sort - Lower Bounds on Parallel Sorting - Odd Even					

Transposition Sort - Bitonic Merge - Sequence, Bitonic Merge on Shuffle Exchange Network - Two-dimensional Mesh Network - Hypercube Network - Parallel Quicksort - Hyperquick Sort.			
<b>Module:6</b>	<b>Graph and Search Algorithms</b>		<b>6 hours</b>
Minimum-spanning Tree - Single-source Shortest Path - All-pairs Shortest Path - Sequential Search Algorithms - Parallel Depth-First Search - Parallel Breadth-First Search.			
<b>Module:7</b>	<b>Parallel Computing Platforms</b>		<b>6 hours</b>
Programming Shared-Memory Multiprocessors with OpenMP - Programming Distributed-Memory Multiprocessors with MPI - Programming Massively Parallel Processors with CUDA.			
<b>Module:8</b>	<b>Contemporary Issues</b>		<b>2 hours</b>
<b>Total Lecture hours:</b>			<b>45 hours</b>
<b>Text Book</b>			
1.	Michael Quinn, Parallel Computing: Theory and Practice, 2017, 2 <sup>nd</sup> Edition, McGraw Hill Education.		
<b>Reference Book</b>			
1.	David B. Kirk, Wen-mei W. Hwu, Programming Massively Parallel Processors: A Hands-on Approach, 2012, 2 <sup>nd</sup> Edition, Morgan Kaufmann.		
Mode of Evaluation: Continuous Assessment Tests, Assignment, Quiz, Final Assessment Test			
Recommended by Board of Studies		12-10-2022	
Approved by Academic Council		No. 68	Date 19-12-2022